

1984 IEEE MICROWAVE AND MILLIMETER-WAVE MONOLITHIC CIRCUITS SYMPOSIUM

San Francisco, CA — May 29-30, 1984



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CALL FOR PAPERS

The IEEE Microwave and Millimeter-Wave Monolithic Circuits Symposium will be held in San Francisco, CA on May 29 and May 30, 1984. This symposium is held annually in conjunction with the IEEE MTT-S International Microwave Symposium.

Papers are solicited describing original work in the field of analog and digital monolithic integrated circuits with current or potential applicability to the 1-300 GHz frequency range. The following subject areas concerned with the design, fabrication and testing of microwave and millimeter-wave monolithic integrated circuits are particularly appropriate for this conference:

- Analog and Digital ICs
- Oscillators and Amplifiers
- Related Device Technology
- Radiation Effects and Reliability
- Device and Circuit Modeling and Design
- Signal Control and Modulation
- Computer-Aided Design Techniques
- Microwave Digital Interfaces
- Packaging and Testing
- Systems, Subsystems and Components

Authors are requested to submit 8 copies of a 500-1000 word summary, with supporting illustrations, that clearly explains their contribution. An abstract of 30 to 50 words should also be submitted. The papers must be received on or before 12 December 1983 by

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USA authors should obtain company and government clearances prior to submission of papers. Notice of acceptance or rejection will be mailed to authors by 6 February 1984. Authors of accepted papers will receive material and instructions for preparing photoready copies of papers to be printed in the Symposium Digest, copyright release forms, and presentation guidelines. The authors are also encouraged to prepare expanded versions of the papers presented at the symposium for consideration for publication in a special section of the MTT-S and/or ED-S Transactions.

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